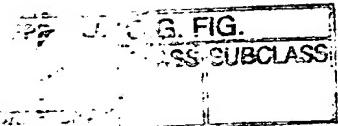


DOT1	WS1	REP1	DOT2	WS2	REP2	DOT3	WS3	REP3	DOT4	WS4	REP4	DOT5	WS5	REP5	DOT6	WS6	REP6	DOT7
101	11	40	37	11	40	37	11	40	37	11	40	37	11	40	37	11	40	37
bit	bit	bit	bit															

WS7	REP7	DOT8	WS8	REP8	DOT9	WS9	REP9	DOT10	WS10	REP10	DOT11	WS11	REP11	.....			
11	40	37	11	40	37	11	40	37	11	40	37	11	40	37	11	40	37
bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit

FIG. 1



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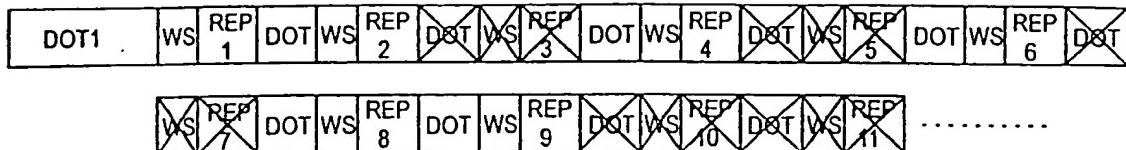


FIG. 2a

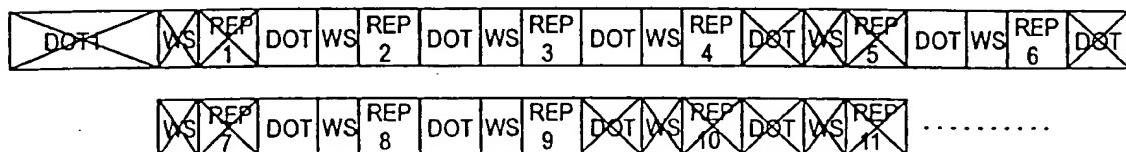


FIG. 2b

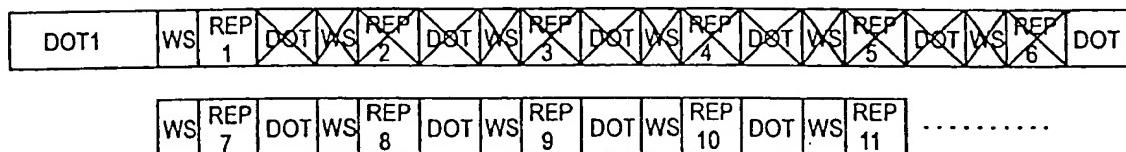


FIG. 2c

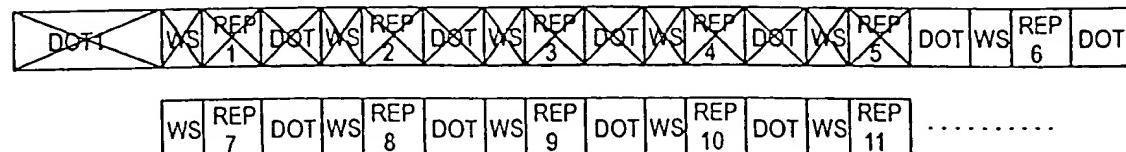


FIG. 2d

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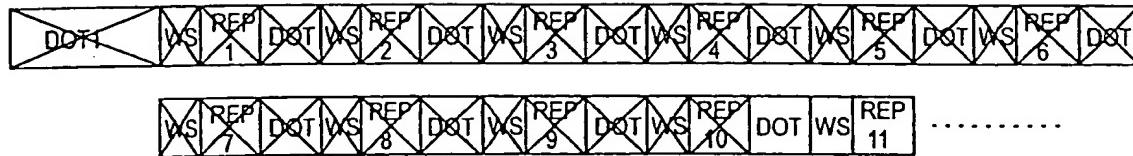


FIG. 3a

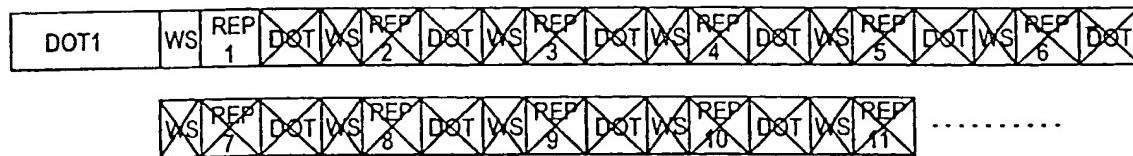


FIG. 3b

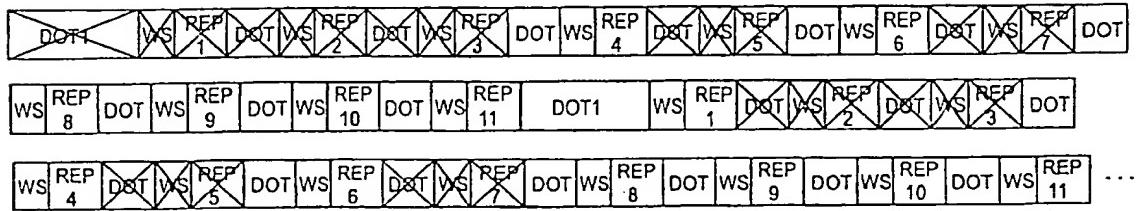


FIG. 4a

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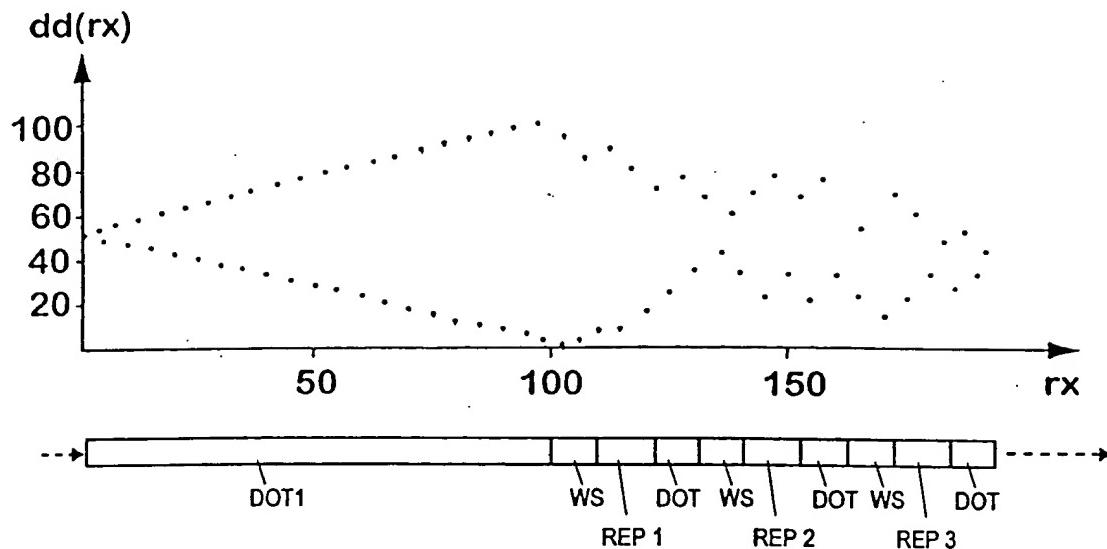


FIG. 4b

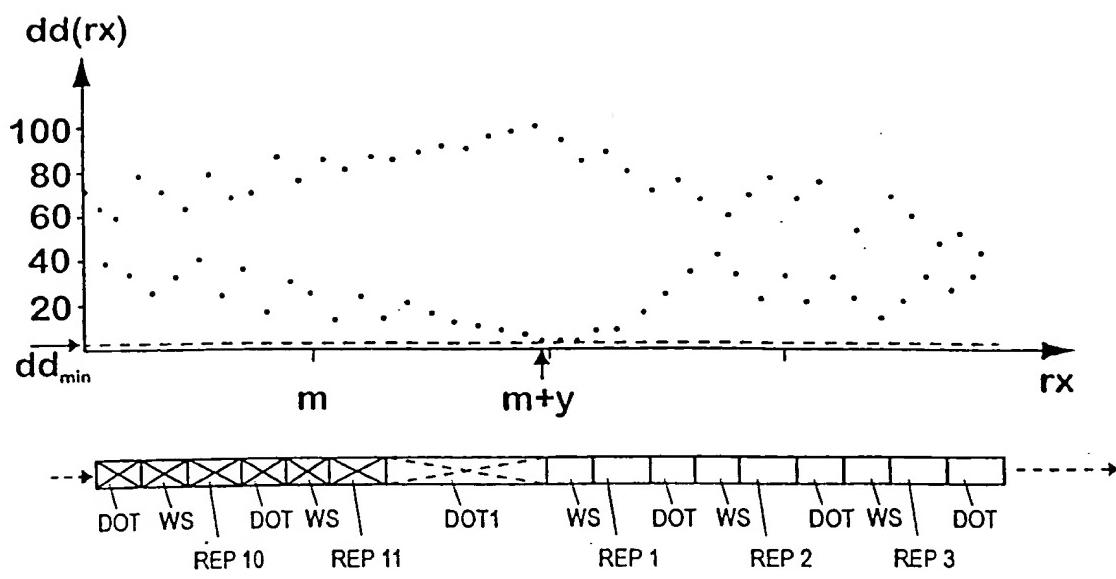


FIG. 4c

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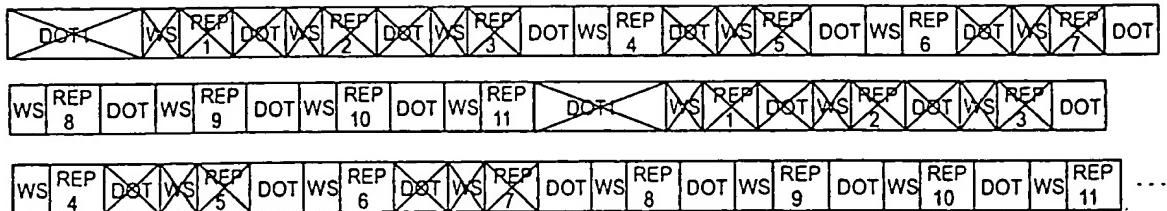


FIG. 4d

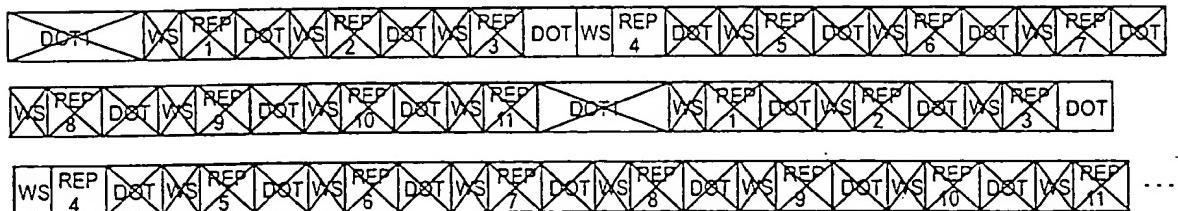


FIG. 5